10/539549

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.

Unassigned

Applicant

Petrus Magnee et al.

Confirmation No. Unassigned JC05 Rec'd PCT/PTO 1 6 JUN 2005

Filed

Concurrently

TC/A.U.

Unassigned

Examiner

Unassigned

Docket No.

BE 020044 US

Customer No.

24738

Title:

Method of Manufacturing a Semiconductor Device

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

Prior to calculation of the filing fee and examination please amend the aboveidentified application as follows:

Amendments to the Specification there are no amendments in this paper.

Amendments to the Claims begin on page 2 of this paper.

Remarks/Arguments begin on page 4 of this paper.